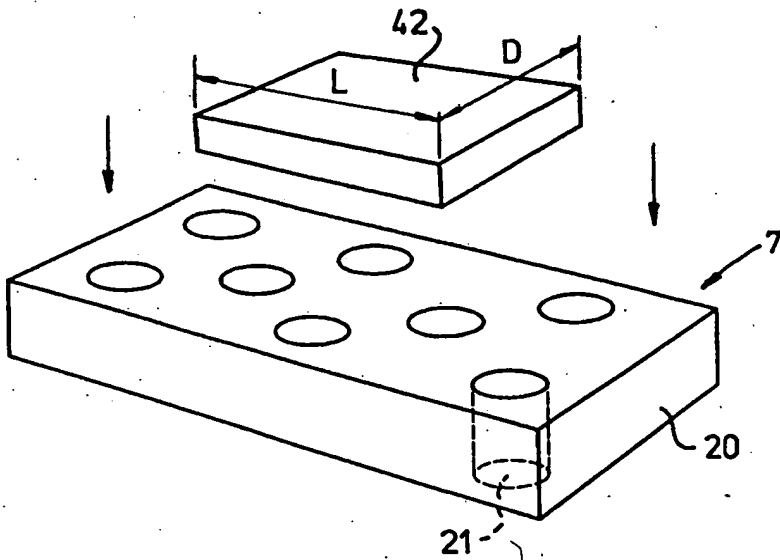




## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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<p>(54) Title: SUBSTRATE HAVING A UNIDIRECTIONAL CONDUCTIVITY PERPENDICULAR TO ITS SURFACE, DEVICES COMPRISING SUCH A SUBSTRATE AND METHODS FOR MANUFACTURING SUCH A SUBSTRATE</p>		
<p>(57) Abstract</p> <p>Substrate (7; 7'; 10) and devices including such a substrate, the substrate having a first surface and a second surface extending substantially in parallel to the first surface, the substrate being of a material of a first conductivity and provided with a plurality of electrically conducting channels (21) which are extending exclusively in a direction perpendicular to the first and second surfaces, said channels having a second conductivity substantially larger than the first conductivity, the substrate being provided with at least one electrode (42) on either one of the first and second surfaces, contacting at least one of said channels, the at least one electrode (42) having a predetermined minimum dimension (D) in a contact area (A) with the substrate, and mutual distances between adjacent ones of the plurality of channels (21) being smaller than said minimum dimension of said at least one electrode (42).</p> 		

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Substrate having a unidirectional conductivity perpendicular to its surface, devices comprising such a substrate and methods for manufacturing such a substrate

5 The present invention relates to a substrate having a first surface and a second surface extending substantially in parallel to the first surface, the substrate being of a material of a first conductivity and provided with a plurality of electrically conducting channels which are extending exclusively in a direction perpendicular to the first and second surfaces, said channels having a second conductivity substantially larger than the first conductivity, the substrate being provided with at least one electrode on either  
10 one of the first and second surfaces, contacting at least one of said channels.

Such a device is known from WO-A-96/04585 which discloses an LCD having an active matrix of electronic components, like thin film transistors and thin film diodes, deposited on the outside surface of the LCD cell substrate.

15 It is to be observed that for the purpose of the present invention, the expression "exclusively in a direction perpendicular to ..." is intended to include deviations inherently due to the production process used. Moreover, the first and second conductivities differ at least by a factor of 1000, but preferably much more, e.g. more than a factor  $10^5$ .

20 US Patent Specification 4,613,351 describes a glass material in which parallel conducting tracks are applied in a predetermined direction. The tracks are intended to deflect and detect electromagnetic radiation having a wavelength between less than 0.1  $\mu\text{m}$  and 1 mm.

25 US Patent Specification 5,438,223 describes a kind of rivet system for interfacial connection of an insulating layer with an electrically conducting material. The electrically conducting parts are applied in holes transversely through the layer and then thermally riveted.

30 Japanese Patent Application JP-A-08/143677 describes the production of a sheet by depositing gaseous material on an electrode. The electrical conductivity in a direction perpendicular to the surface of the sheet is much greater than the electrical conductivity in a direction parallel to the surface of the sheet. However, the conductivity in the direction perpendicular to the surface is in the order of  $10^{-6}$  S/cm, which is very low and in the semiconductor range.

US Patent Specification 5,272,217 describes the use of anisotropic polymers in

a sheet. The conductivity parallel to the surface of the sheet of the polymer is much higher than the conductivity in a direction perpendicular to the surface of the sheet. The tetrathiotetracene complex is mentioned as one of the polymers used. Use is made of a "stack orientation" in which elements of very small dimensions are grouped head-to-tail.

US Patent Specification 5,556,706 also describes particles of very small dimensions which are oriented head-to-tail in a direction parallel to the surface of a polymer sheet. The sheet is produced by depositing a gaseous material on an electrode. Here, again, there is a greater conduction in a direction parallel to the surface of the sheet than in a direction perpendicular thereto after production.

US Patent Specification 5,229,635 describes a device and a method of orienting very small elements head-to-tail under the influence of an electrical field. Said patent specification is aimed at achieving an electrical conductivity in a predetermined direction parallel to the surface of the sheet. Incident light can be converted into electrical power with the sheet.

Japanese Patent Application 08/007658 describes an adhesive film which is provided with small conducting particles. Prior to use, the film is non-conducting because the conducting particles are not in contact with one another. The film is used to make a connection between the terminal pins of an IC and a substrate. Because the terminal pins of the IC are pressed firmly in the direction of the substrate, electrically conducting connections are produced between the terminal pins of the IC and the conducting particles in the film. As a result of pressing the terminal pins firm enough against the substrate, a conducting connection is produced between the terminal pins and desired electrically conducting tracks on the substrate. At those points where there are no IC terminal pins, the film remains non-conducting. Similar adhesive films are disclosed in US Patent Specification 5,213,715 and Japanese Patent Application 57/111366 and 05/011265.

Figure 1 shows in a very diagrammatic way the structure of a conventional LCD. The centremost of the three layers shown, indicated by the reference numeral 3, is the optical layer. This is surrounded on either side by two controlling layers 2, 2'. The controlling layers 2, 2' must be situated as closely as possible to the optical layer 3. In essence, there are two types of optical layers: liquid crystals, which orient themselves under the influence of an electrical field, and light-emitting layers, which

emit light under the influence of an electrical current. Light-emitting layers dissipate power during operation, while liquid crystals dissipate energy solely for the purpose of orientation. Light-emitting materials are, for example, LEDs, laser diodes and electroluminescent materials.

5           Where polarized light is employed, polarization filters are in practice also needed. Said polarization filters and any other correction filters are not described in more detail here because they are not of importance for the present invention. However, where necessary they can in fact be used.

10           The optical layer 3 generally comprises three layers 4, 6, 4', as shown in Figure 2. In addition, supporting layers 1, 1' are situated on the outside of the controlling layer 2, 2'. The centrally situated layer 6 comprises a liquid-crystal layer in which spacers 5 are situated at regular or irregular distances in order to keep the two insulating layers 4, 4' at a predetermined distance from one another. The primary function of the layers 4, 4' is to facilitate the orientation of the liquid crystals in the same direction. Furthermore, they are important in preventing the liquid crystals from  
15           being contaminated by migration of ions, such as tin and indium, out of the conductor patterns 2, 2'. Finally, the layers 4, 4' provide an insulating function.

20           The controlling layers 2, 2' comprise, for example, transparent material which contains a pattern of, for example, parallel, likewise transparent conductors. The parallel conductors in the controlling layer 2 are then situated, for example, perpendicular to the transparent, parallel conductors in the controlling layer 2'. Using such patterns, electrical fields can be generated at desired positions transversely or at an angle to the liquid crystal 6 layer, as a result of which the crystals in the liquid crystal layer 6 orient themselves. At the points where this occurs, the liquid crystal  
25           layer 6 becomes impenetrable to incident light.

30           In the conventional structure of an LCD shown in Figure 2, the controlling layers 2, 2' are situated inside the supporting layers 1, 1'. Such LCDs can be transported only when the supporting layers have been applied. Without the supporting layers 1, 1', the LCD structure would be too vulnerable. As a result, it is impossible to make changes and/or corrections in the controlling layers 2, 2' after the manufacture of the LCD.

          In addition, before the controlling layers 2, 2' have been applied, it is impossible to carry out checks on the correct operation of the liquid crystal layer 6.

Moreover the production yield of LCDs according to the known structure is low. A high percentage, sometimes more than 70%, does not meet the requirements and cannot be sold.

5 A standard method of orienting the material of the insulating layers 4, 4' horizontally is the so-called "rubbing" or frictional treatment. However, because the controlling layers 2, 2' in the conventional arrangement are applied next to the insulating layers 4, 4', there is a high risk of damage to said controlling layer 2, 2' during the rubbing. This arises both as a result of discharge as a consequence of static electricity and as a result of other types of damage.

10 The controlling layers 2, 2' are generally composed of indium/tin oxide structures and/or thin-film transistors and/or metal/insulation/metal structures and/or diodes. Polyimide is often used as insulating layer 4, 4'. High temperatures are used during the process for applying the last-mentioned layer, which is an additional hazard for the satisfactory operation of the thin-film transistors, the metal/insulation/metal  
15 structures and/or diodes.

The devices disclosed in WO-A-96/04585 referred to above and which are provided with active matrices on the outside surfaces of the LCD cell substrates at least partly solve these problems related to the conventional LCD design. However, still a pattern of individual, transparent electrodes is applied on the inside surface of  
20 the LCD cell substrate according to this prior art document. These internal electrodes are connected to the active matrix on the outside of the surface by means of thin conductive leads. Each internal electrode is connected to one single thin conductive lead, whereas each of the thin conductive leads is also connected to one of a series of conductive leads applied to the outside surface of the LCD cell substrate and  
25 contacting the active matrix.

No manufacturing details with respect to producing the thin conductive leads in the LCD substrate are given. No other production methods than multi-mask steps vacuum technology in general is referred to. Therefore, the suggested method of producing the devices of WO-A-96/04585 necessarily includes extremely difficult  
30 alignment steps for properly aligning the thin conductive leads through the LCD substrate with the internal transparent electrodes and the external conductive leads. Especially, it seems difficult if not impossible to transport unfinished LCD's to a customer without having an active matrix on the outside surface of the LCD substrate

and leave it to the customer to apply the active matrix on the outside surface of the LCD substrate in correspondence with his needs. Without undue costs, costumers will not be able to apply their desired active matrix in proper alignment with the thin conductive leads through the substrate.

5       The primary object of the present invention is to provide a substrate with at least one electrode, the substrate being provided with electrical leads perpendicular to its surface and allowing electrical connection between at least one electrical lead and the electrode without complex alignment procedures.

10       For this purpose, a substrate of the type mentioned at the outset is characterised in that the at least one electrode has a predetermined minimum dimension in a contact area with the substrate, and that mutual distances between adjacent ones of the plurality of channels are smaller than said minimum dimension of said at least one electrode. Then, no matter where the at least one electrode is contacting the substrate it will always contact at least one of the channels. This greatly reduces alignment  
15       problems.

Preferably, the mutual distances are at most two times smaller than the minimum dimension of at least one electrode.

Even more preferably, the mutual distances are at most ten times smaller than the minimum dimension of the at least one electrode.

20       The plurality of electrically conducting channels may either be distributed in accordance with a regular pattern or randomly through the substrate.

In one embodiment, the mutual distances are smaller than 3.5  $\mu\text{m}$ .

A further object of the invention is to provide a device for displaying information, for example an LCD, which can be transported to customers without the  
25       pattern of controlling layers necessarily already having been applied on the substrate and still providing customers with the possibility to apply the pattern of controlling layers on locations desired by them without undue costs and complexity.

A still further object of the invention is to increase the yield of the production of such devices.

30       To that end, the invention also relates to a device for displaying information, comprising at least:

- a first layer which extends in one plane and is composed of a material whose optical properties vary under the influence of an external electrical control

system in such a way that either the transparency of portions selected with the electrical control system of the first layer for light incident thereon varies or portions selected with the electrical control system of the first layer emit light; a second and a third layer which extend substantially parallel to the plane, are situated on either side of the first layer and impart to the device a certain desired rigidity, at least one of the second and third layers being provided with at least one electrode and with electrically conducting channels which are conductive exclusively in a direction perpendicular to the plane, said at least one electrode electrically contacting at least one of said channels, characterized in that said at least one electrode has a predetermined minimum dimension in the contact area with said at least one of the second and third layers, and that mutual distances between adjacent ones of the plurality of channels are smaller than said minimum dimension of said at least one electrode.

Such a structure has sufficient rigidity to be able to be transported to customers without the ultimately necessary conductor patterns having already been applied. The conductor patterns can be applied by the customer himself on the outside of the second and/or third layer. The conducting channels in the second and/or third layer then ensure that an electrical voltage and/or current whose positions are defined by the pattern of conductors is transmitted to the first layer. The narrower the small channels and the closer together they are situated, the greater the resolution will be.

A further advantage is that, if the pattern of conductors is not correctly applied to the structure thus defined, said pattern can easily be removed without the device as a whole having to be discarded. After removal, the pattern can be applied again. This will appreciably increase the production yield.

The device according to the invention may be a component of an LCD, in which the first layer is a liquid-crystal layer, provided with spacers and in which there is situated between said one of the said second and third layers and the first layer a fourth layer which is made of an electrically insulating material. Said liquid-crystal layer may be of the nematic or smectic type, depending on the desired use. Nematic liquid-crystal layers require a continuous drive in order to be able to have a memory function. This requires suitable electronics, comprising, for example, metal/insulator/metal structures, thin-film transistors, diodes and conductors. Smectic liquid-crystal layers have a spontaneous memory function.



As an alternative, such an LCD may have a polymer dispersed liquid-crystal layer as first layer, a fourth layer which is made of an electrically insulating material being situated between said one of the second and third layers and said first layer.

As a further alternative, the device according to the invention can be designed  
5 so that the first layer emits light under the influence of an electric current.

The device according to the main claim of this invention defined in this way and according to the variants mentioned thereof then forms an unfinished plate of material which can subsequently be provided by the user with the necessary application-dependent conductor tracks and electronics. Such an unfinished device can  
10 therefore be made as a standard and not in an application-dependent way. This increases the flexibility of the possible applications.

In some applications, not only said one of the second and third layers, but also the other, will have a pattern of electrical conductors which is such that the electrical conductivity of the conductors is directed exclusively in a direction perpendicular to  
15 the plane.

In one of the embodiments of the device according to the invention, there is applied to said one of the said second and third layers a photoconducting layer, to the outside of which a transparent electrically conducting layer has been applied. Such a device can be used as electronic paper.

20 In the case of the device last mentioned, a light source, for example a laser, can be provided to generate a light beam for exposing predetermined locations in the photoconducting layer. Such a device forms a laser-beam display.

The device defined above which is provided with a photoconducting layer to which a transparent, electrically conducting layer has been applied can be used with a  
25 laser for manufacturing a mask for photolithographic purposes. The advantage of such a mask is that the locations which are or are not transparent to light can always be defined again without altering the position of the device. Such a mask can therefore advantageously be used in photolithographic processes, because not moving the mask benefits the accuracy.

30 The device according to the invention can easily be provided with colours on one of the surfaces by applying a photosensitive layer to the side of the device at which the other of the second and third layers is situated. The locations at which the layer has to remain as a coloured layer and those at which it has to be removed can

then easily be defined with the aid of exposure procedures and chemical development procedures.

Claims 22 to 26 inclusive define methods for manufacturing a layer of material which extends in one plane and is provided with a pattern of electrical conductors extending exclusively in a direction perpendicular to the surface.

The present invention will be explained in greater detail below by reference to some figures which are intended to illustrate the invention and not to limit the scope of its protection.

Figure 1 shows a diagrammatic view of a conventional LCD;

Figure 2 shows the layer structure of a conventional LCD in greater detail;

Figure 3 shows an LCD having a layer structure according to the present invention;

Figure 4 shows an alternative structure for the layer structure of Figure 3;

Figure 5 shows the application of a light-emitting layer in the device according to the invention;

Figures 6a and 6b show alternative embodiments of a device according to the invention which can be used as electronic paper and also as laser-beam display;

Figure 7 shows a device according to the invention to which a coloured layer has been applied;

Figures 8a, 8b, and 8c show diagrammatically the structure of an electrode and a layer which is provided with a pattern of conductors, which conductors extend exclusively in a direction perpendicular to the surface of the layer;

Figures 9a, 9b, 9c, 10, and 11 inclusive show alternative methods for forming a layer such as that shown in Figures 8a-8c;

Figure 12 shows the use of a layer as shown in Figures 8a-8c as printed circuit board (pcb).

Figure 3 shows an LCD in accordance with the present invention. In the layer structure according to Figure 3, the controlling layers 2, 2' have not been applied directly to the insulating layers 4, 4', but an additional layer 7, 7' is situated in between. Said additional layer is formed from a material in which there are conductors directed exclusively in a direction perpendicular to the surface of the layers 7, 7'. Such a layer 7 is shown diagrammatically in Figures 8a and 8b. The layer 7 is therefore composed of an insulating material 20 which contains small conducting channels 21

which extend exclusively in a direction perpendicular to the surface of the layer. Preferably, both the insulating layer 20 and the small conducting channels 21 are transparent. The cross section of the small channels 21 and the mutual spacing of the small channels 21 determine the resolution of the device in which the layer 7 is used.

5        Figure 8a shows a perspective view of insulating layer 20 with channels 21, and an electrode 42 which is to be connected to the layer 20 at a predetermined location. Figure 8b shows the electrode 42 at the predetermined location. The electrode 42 may have any desired shape. However, in accordance with the invention there is a relationship between the dimension of the electrode 42 and the mutual distance  
10        between adjacent channels 21 such that no matter where electrode 42 contacts layer 20 it will always contact at least one channel 21. This reduces alignment problems seriously.

      In case electrode 42 has a rectangular shape, as shown in figure 8a, with width D and length L,  $L > D$ , this condition is met when the mutual distance between  
15        adjacent channels 21 is  $< D$ . To be sure that a good electrical contact between electrode 42 and at least one channel 21 will be established, preferably, that mutual distance is at most  $\frac{1}{2}D$ . Even more preferably, that mutual distance is at most  $\frac{1}{10}D$ .

      In the arrangement according to Figure 3, the layer 7 ensures support, which support was provided in the arrangement according to Figure 2 by the layer 1. In other  
20        words, in the arrangement according to Figure 3, the controlling layers 2, 2' are situated on the outside of the device. The controlling layers 2, 2' are therefore applied later than the insulating layers 4, 4'. The high temperatures occurring during the application of the insulating layers 4, 4' and the high electrostatic voltages occurring as a result of the "rubbing" procedure of the insulating layers 4, 4' therefore no longer  
25        have any influence on the controlling layers 2, 2'.

      Electrodes of the conducting pattern inside the controlling layer 2 are connected to the small channels 21 inside the layer 7.

      Figure 8c further illustrates this. Figure 8c shows a cross section through insulating layer 20 with conducting channels 21 above liquid-crystal layer 6. Here, the  
30        mutual distance between the channels 21 is much smaller than the dimension D of electrode 42. A voltage applied on electrode 42 will be transmitted, as it were, perpendicular in the direction of liquid-crystal layer 6 and be present at the terminating portions of the channels 21 at the interface between layer 7 and layer 6. Therefore, in

an area indicated by "A" in figure 8c a plurality of channels present an electrical voltage to the liquid-crystal layer 6, thus forming a kind of virtual electrode. Therefore, there is no need to apply an additional electrode in area A to present the voltage to liquid-crystal layer 6. This facilitates the manufacturing process of liquid crystal display devices. Moreover, locations where voltages need to be presented to liquid-crystal layer 6 are only determined by the location of electrodes 42 on the outside surface of the layer 7, which facilitates achieving a desired resolution. The number of channels and there mutual distances, as well as the size of the electrodes 42 determine the resolution actually obtained.

The device according to Figure 3 can be marketed without the presence of the controlling layers 2, 2'. Such a device thus comprises only the layers 7, 4, 6, 4', 7' and still has a universal possible application. Any user can apply the pattern of conductors he desires by means of the controlling layers 2, 2'.

It is also possible to market a structure in which only one of the layers 7, 7' is applied. The layer 7' is then, for example, absent. At the point where the layer 7' is situated, a user can apply a transparent conducting pattern according to his own design.

Figure 4 shows an alternative to such an unfinished LCD structure. In place of the liquid-crystal layer 6 having spacers 5, the device according to Figure 4 is provided with a polymer dispersed liquid-crystal layer 8.

It is obvious that one of the two layers 7, 7' can be replaced by a transparent, completely electrically conducting layer also in the arrangement according to Figure 4.

The devices according to Figures 3 and 4, or the variants thereof indicated above, can be provided on one side thereof with a light-reflecting layer. This produces light-reflecting LCDs. Light-reflecting LCDs provide a more natural viewing characteristic, are less tiring and are superior to conventional viewing screens in the event of increase in the illumination level. Information presented by light-reflecting LCDs is also more readily absorbed by users than information presented by transparent LCDs and/or light-emitting LCDs.

Figure 5 shows a device according to the invention in which a light-emitting layer 11 is applied instead of a liquid-crystal layer 6 having spacers 5 or a liquid-crystal layer 8. Said light-emitting layer 11 has the property that it emits light at those locations at which an electrical voltage is applied perpendicularly to the surface of the light-emitting layer 11.

Figure 5 again shows an unfinished basic structure. The basic structure can be completed by the customer by applying the controlling layers 2, 2' in the form he desires. One of the two layers 7, 7' can also be replaced if required by a layer which is completely transparent and is completely electrically conducting. As a further  
5 alternative, a light-reflecting layer can be applied to one of the two outsides of the layers 7, 7' so that the device according to Figure 5 will emit light only on one side.

Figures 6a and 6b show devices which can be used as electronic paper or as laser-beam display.

The reference numerals in the device according to Figures 6a and 6b have the  
10 same meaning as in the preceding figures. A photosensitive layer 9 has been applied to the layer 7. A transparent, electrically conducting layer 10 is situated on the outside of the photosensitive layer 9.

The device according to Figure 6a operates as follows. The device is exposed to light 13 which strikes the photosensitive layer 9 via the electrically conducting layer  
15 10. At the points where light is incident on the photosensitive layer 9, the latter becomes conducting. At those points where no light is incident, the photosensitive layer 9 remains an insulator. At those points where light first falls and is interrupted, the insulating state returns. The electrical connections between the conductors 21 in the layer 7 and the electrically conducting layer 10 are thus defined, as it were, by means  
20 of the photosensitive layer 9. A desired pattern of the electric field in the liquid-display layer 6 can thus therefore also be defined by applying voltage to the electrically conducting layer 10 and the layer 7'. If the liquid-crystal layer 6 is of the smectic (ferroelectric) type, the image thus recorded will be retained because smectic  
LCDs have a memory function even if the voltage is switched off. If nematic LCDs  
25 are used, application of suitable electronics is necessary to achieve the memory function.

A light-reflecting layer can again be applied to the underside of the device according to Figure 6a. Layer 7' itself may also be made of light-reflecting material. The layer 7' can be replaced by a layer which is completely electrically conducting  
30 and is optionally reflecting or transparent.

The transparent type can be used, for example, as a transparency which is used in the case of slides for overhead projectors.

The advantage of the "electronic paper" according to Figure 6a is that the

image recorded therein can be replaced by another image as a result of repeating the above-mentioned procedure.

Figure 6b shows an alternative to the device according to Figure 6a. The only difference between the layer structures according to Figures 6a and 6b is that, in Figure 6b, a polymer dispersed liquid-crystal layer 8 is used instead of the liquid-crystal layer 6 having spacers 5.

Another difference between Figures 6a and 6b as a whole is that a laser 12 which emits a laser beam 14 in the direction of the photosensitive layer 9 is shown in Figure 6b. Since the cross section of the laser beam 14 can be very restricted an image can be written very accurately in the photosensitive layer 9 with the laser 12. This benefits the total resolution. At those points where the laser beam strikes the photosensitive layer 9, the latter becomes conducting. At those points at which the laser beam is absent, the photosensitive layer 9 remains an insulator. Electronic paper can then be defined in the same way as in the device according to Figure 6a.

It is pointed out that, in the devices according to Figures 6a and 6b, the layer 7 can in principle be omitted. The light beam 14 then makes a channel in the layer 9 with which an electrical voltage can be transmitted directly to the layer 4 without the intervention of layer 7.

An interesting application of the layer structure according to Figure 6a or 6b with the laser 12 is that of a laser-beam display. The laser beam 14 which is generated by the laser 12 can specifically write an image composed of various image points in the photosensitive layer 9, which image points are sequentially transmitted to the liquid-crystal layer 6 or the polymer dispersed liquid-crystal layer 8 at the instant the voltage is applied between the layers 10 and 7'. Each image point can always be replaced by a subsequent image point by always writing an image point again in the photoconducting layer 9 with the aid of the laser beam 14 and then transferring it to the liquid-crystal layer 6 or the polymer dispersed liquid-crystal layer 8. The resolution of such a screen is particularly high and only restricted by the cross section of the small channels 21 in the layer 7 (or 7') and their mutual spacing.

A light-emitting layer 11 can again also be used instead of the liquid-crystal layer 6 with spacers 5 and the polymer dispersed liquid-crystal layer 8 (not shown). Such a light-emitting layer can emit light of one colour. As an alternative, however, said light-emitting layer may be constructed so that coloured images can be displayed,

just as in the case of conventional coloured-image tubes.

The displays fabricated in this way have the same structure everywhere, as a result of which they are insensitive to variations in the positioning of the laser beam, which is not the case for conventional image tubes.

5       The resolution of such laser-beam displays is particularly high.

10       The devices according to Figures 6a and 6b can advantageously be used as mask in photolithographic processes. After all, a desired pattern of opaque and transparent portions can be defined in the liquid-crystal layer 6 or the polymer dispersed liquid-crystal layer 8 with the aid of the laser beam 14. If such a device  
15       whose pattern defined in this way in the liquid-crystal layer 6 or the polymer dispersed liquid-crystal layer 8 corresponds to the pattern of a certain photolithographic mask is placed above a semiconductor device provided with a photoresist layer, said photoresist layer can be exposed using a different light beam. Said different light beam will then transfer the pattern recorded in the layer 6 or 8 to the photoresist layer on the  
20       semiconductor. Said photoresist layer can then be developed and treated in a known manner. Structures can be transferred in this way to the semiconductor device using a mask which, in principle, does not have to change position. The pattern on the mask formed in this way can simply be altered by writing a new pattern in the photosensitive layer 9 using the laser beam 14 and transferring it to the layer 6, 8 by  
25       applying the correct voltage between the layers 10 and 7'. In this way, new masks do not always have to be used each time in subsequent photolithographic steps. After all, in the device a new mask can always be defined without altering the position of the latter. This benefits the accuracy.

25       Figure 7 shows a device in which the reference numerals corresponding to the reference numerals in the preceding figures have the same meaning.

30       The device is provided with an underlayer 52 which is a photosensitive cover layer. A certain pattern of positions which are opaque or transparent to light 13 is recorded in the layer 6, 8 in the manner described above. The light 13 then exposes the photosensitive cover layer 52 only at those points where the layer 6, 8 is transparent. As a result a copy of the pattern in the layer 6, 8 is produced on the photosensitive cover layer 52. The photosensitive cover layer 52 can then be developed in a known chemical way so that a copy of the image is produced.

The unexposed portions of the cover layer 52 can be removed with the aid of

selective etching procedures. As will be clear to a person skilled in the art, as a specific alternative the exposed portion of the cover layer 52 can be removed with the aid of selective etching procedures.

One or more coloured layers can also be applied as desired to the points where the layer 52 has been removed. Furthermore, metal/ insulator/metal structures, thin-film transistors, diodes and/or conductors can be applied in this way.

It is pointed out that the layer 7 can in principle also be omitted in the structure according to Figure 7.

Figures 9a and 9b show a possible method for manufacturing a layer, as shown in figures 8a-8c.

There is provided a layer 7 from a material which has a predetermined starting conductivity. However, the conductivity of the selected material can be changed substantially by illuminating the material with radiation of a predetermined wave length.

In a first step a mask 23 is provided on the layer 7. The mask 23 is provided with a plurality of portions 24 which are non-transparent to radiation of that predetermined wave length. Then, the mask 23 and the layer 7 are illuminated with radiation 22 having that predetermined wave length. The radiation 22 is directed to the mask substantially perpendicular to its surface.

Those portions of the layer 7 which are illuminated by radiation 22 will obtain a substantial lower conductivity than the starting conductivity whereas those portions, indicated with reference sign 21, which are not illuminated will retain the starting conductivity. Thus, by providing the portions 24 in the mask 23 with the proper size and proper mutual spacings, a desired pattern of conducting channels 21 in layer 7, having a desired channel width and mutual spacings, can be made.

In a first example, pure PANi/CSA (camphor sulphonic acid) films were used as layer 7. Different films of PANi/CSA were exposed to 1, 2, and 14 hours respectively, to UV light 22. The starting films had a conductivity of  $\pm 40$  S/cm. The films were exposed for 1, 2, and 14 hours, respectively, and showed conductivities of 3, 1, and  $10^{-4}$  S/cm, respectively. The conductivity of the films was measured using a two-probe method.

This shows that a reasonably high anisotropy in conductivity can be obtained by UV exposure. Probably, the PANi/CSA is partially transformed by the UV light to



non-conducting emeraldine base.

A test layer 7 was also made in which the portions 24 were transparent to UV light 22 whereas the remaining portion of mask 23 was non-transparent to the UV light 22. Thus, in this experiment the channels 21 became non-conducting. The sample thus  
5 obtained was checked on the appearance of topological changes (caused by ablation), both by AFM and SEM. The SEM measurements, conducted without gold coating, did not show topological irregularities, giving an indication that no significant ablation occurred. However, the AFM measurements showed small topological changes. These changes are shown in figure 9b, which shows an AFM image (contact measurement) of  
10 an area of a PANi/CSA film exposed to UV light for 14 hours through a mask comprising holes with diameters of  $\pm 1$  micrometer at mutual distances of 3.5 micrometer.

Small dots 25, having light grey colour in figure 9b, with approximately the diameter of the mask holes are visible in figure 9b, having an average height of 50-  
15 100 nanometre, which is most probably due to heat-induced surface swelling. Why this was not observed in the SEM measurements is not clear at the moment. Most probably, the absence of a thin gold coating (to provide conductivity) on top of the sample resulted in poor imaging of the non-conducting phase. The large white spots 26 in figure 9b are due to glass particles stemming from a diamond cutter used for cutting  
20 to make the sample fit on an AFM sample holder.

A second experiment was conducted with so-called ORMECON® lacquer of Zipperling. This complicated lacquer system is based on a dispersion of PANi/DBSA (DodecylBenzeneSulphonicAcid) in, most probably, p-xylene and other cosolvents. A thin film of the lacquer was spincoated at a rotation speed of 3500 rpm on a glass  
25 plate. This resulted in a green film with a thickness of approximately 100 micrometer and a resistance of 7 k $\Omega$ . Measurement was done with a simple voltmeter and two point probes. A film could be easily peeled of the glass surface and had good mechanical properties. The film was exposed to UV light for approximately 1.5 hour, resulting in a colour change from green to black/blue. No surface resistance could be  
30 measured with the simple voltmeter indicating that the conductivity was seriously diminished, i.e., resistance > 20 M $\Omega$ . Conductivity measurements with evaporated gold contacts are scheduled to provide exact conductivity values.

A third experiment was also conducted with ORMECON® lacquer. A structure similar to the structure of figure 3 was made in the following way. A thin electrode was applied to a glass substrate. On the thin (aluminium) electrode a layer of ORMECON® lacquer was applied, the ORMECON® lacquer being patterned in a way indicated in the preceding paragraph. On this ORMECON® lacquer layer a layer with spacers and polymer dispersed liquid-crystal was applied. An ITO was applied on the last layer. Finally, a glass layer was applied to the ITO layer. By means of an optical microscope an image of the arrangement thus obtained was made. Figure 9c shows the result. Figure 9c shows a pattern of conducting channels (light grey colour) surrounded by an isolating material (dark grey colour).

The above-mentioned preliminary results point out that for the lacquer the non-conductive PANi state is formed much more efficient than for PANi/CSA. The most important difference between the lacquer and PANi/CSA is the fact that in the lacquer DBSA is used as the dopant. Most probably, the UV light 22 destructs or evaporates the dopant and creates the non-conducting emeraldine base, rather than destroying the conductive polymer itself.

The intensity of the UV light 22 used was about 40 mW/cm<sup>2</sup>.

Figure 10 shows an alternative method of forming a layer 7.

A mixture of matrix material provided with a solvent and a conductor additive 34 is applied to a substrate 32. At the beginning of the method, the mixture is still in liquid form.

The matrix material may, for example, be polycarbonate. However, the invention is not limited thereto. The conductor additive 34 may, for example, be a tetraselenotetracene complex (TSeT). However, other materials are also possible for this purpose.

A sheet is formed by pouring ("casting") the liquid onto the substrate 32. An electrical field perpendicular to the surface of the substrate 32 is generated at a predetermined position using a capacitor plate 33 opposite the conducting substrate 32. The substrate 32 is moved as indicated by an arrow in Figure 10 so that the liquid matrix material 31 is introduced into the electrical field.

The solvent in the matrix material 31 will then evaporate, as a result of which the structure will become hard. Instead of spontaneous curing, forced curing can also be used, for example with the aid of ultraviolet light. Two-component curing can also

be used.

In the liquid matrix material 31, there are produced conducting particles of the additive 34 which, under the influence of the generated electrical field 30, acquire a positive charge at that side which is closest to the negative side of the electrical field and a negative charge at that side which is closest to the positive side of the electric field. As a result, the particles 34 orient themselves and they form long chains 35 of head-to-tail bonds. Said chains will extend parallel to the electrical field as a result of the electrical field 30 present. A regular intermediate spacing between the chains 35 will, in addition, be produced as a result of the mutual repulsion of said chains. This results finally in a large number of parallel conductors which have no mutual connections so that channels 21 (see Figure 8) are produced in the layer. On the substrate 32 there is produced in this way the desired layer 7 which can be removed from the carrier 32 with the aid of any known procedure. The distance between the chains 35 is dependent on the mixing ratio between the matrix material and the conductor additive. The cross section of the chains 35 which are ultimately formed and which form the small channels 21 depends on the conductor additive chosen. Since the chains are formed at molecular level, the cross section can, however, be particularly small. The mutual spacings can also be made very small, which will increase the resolution of devices equipped therewith. If the cross section of the chains 35 and their mutual spacings are very small, it is of less importance whether all the chains 35 are formed completely correctly, i.e. whether they are all completely conducting. A small percentage of failures can be accepted because many chains grouped together next to one another will nevertheless be used for the ultimate applications. A pixel (image point) defined by conductor pattern 2, 2' on top of the layer 7 in, for example, the embodiment of Figure 3 will in practice be transmitted to the liquid-crystal layer 6 by many parallel chains 35.

Figure 11 shows a third method of making a layer 7. A pattern of a liquid composed of a matrix material containing solvent and a conductor additive 34 is printed on a substrate 32 at predetermined points. The materials used for this purpose may be the same as mentioned above with reference to Figure 10. Dots are printed in this way at a minimum distance from one another and with a minimum cross section. The dots are dried by the evaporation of the solvent. In this process, for example, additional heat may be supplied. After drying, the dots are completely conducting. In

this case, no use is therefore made of an external electrical field.

5 The gap between the dots is then filled with an insulating material 36. Polycarbonate, for example, can be used for this purpose. This can also be done using known printing procedures. Said insulating material is then also dried. The desired layer 7, which can be removed from the carrier 32 with the aid of any known procedure, is produced in this way on the substrate 32. The method in accordance with Figure 11, too, allows the cross sections of the channels 21 and their mutual spacing to be made very small.

10 The layer 7 obtained by the methods outlined above can be used not only for the devices described above. If the layer 7 is sufficiently thick, it is rigid enough to be treated separately and, as shown in Figure 12, it can also be used, for example, as substrate for electronic components, such as chips 41. Such components can then be applied to either side of the substrate and mutually interconnected at desired positions by means of the small conducting channels 21 and conductor patterns 42 applied to the substrate. Chips 41 with their terminals 43 can be mounted on the layer 7 or on the conductor patterns 42 by means of a conducting paste which cures after application.

15 It will be clear to the person skilled in the art that the description given above is given only by way of example and that the scope of protection of the invention is limited only by the definition of the claims.

## Claims

1. Substrate (7; 7'; 10) having a first surface and a second surface extending substantially in parallel to the first surface, the substrate being of a material of a first conductivity and provided with a plurality of electrically conducting channels (21) which are extending exclusively in a direction perpendicular to the first and second surfaces, said channels having a second conductivity substantially larger than the first conductivity, the substrate being provided with at least one electrode (42) on either one of the first and second surfaces, contacting at least one of said channels characterized in that said at least one electrode (42) has a predetermined minimum dimension (D) in a contact area (A) with the substrate, and that mutual distances between adjacent ones of the plurality of channels (21) are smaller than said minimum dimension of said at least one electrode (42).
2. Substrate according to claim 1 wherein said mutual distances are at most two times smaller than said minimum dimension of said at least one electrode (42).
3. Substrate according to claim 1 wherein said mutual distances are at most ten times smaller than said minimum dimension of said at least one electrode (42).
4. Substrate according to any of the preceding claims wherein said plurality of electrically conducting channels (21) is distributed randomly through the substrate.
5. Substrate according to any of the preceding claims wherein said mutual distances are smaller than 3.5  $\mu\text{m}$ .
6. Device for displaying information, comprising at least:
  - a first layer (6; 8; 11) which extends in one plane and is composed of a material whose optical properties vary under the influence of an external electrical control system in such a way that either the transparency of portions selected with the electrical control system of the first layer for light incident thereon varies or portions selected with the electrical control system of the first layer emit light;

- a second and a third layer (7, 7'; 7, 10) which extend substantially parallel to the plane, are situated on either side of the first layer (6; 8; 11) and impart to the device a certain desired rigidity, at least one (7) of the second and third layers having a first conductivity and being provided with at least one  
5 electrode (42) and with electrically conducting channels (21) which are extending exclusively in a direction perpendicular to the plane, said channels having a second conductivity substantially larger than the first conductivity, said at least one electrode (42) electrically contacting at least one of said channels (21),

10 characterized in that said at least one electrode (42) has a predetermined minimum dimension (D) in a contact area with said at least one (7) of the second and third layers, and that mutual distances between adjacent ones of the plurality of channels (21) are smaller than said minimum dimension of said at least one electrode (42).

15 7. Device according to Claim 6, characterized in that the first layer is a liquid-crystal layer (6) provided with spacers (5) and in that a fourth layer (4) which is made of an electrically insulating material is situated between one (7) of the second and third layers mentioned and the first layer (6).

20 8. Device according to Claim 6, characterized in that the first layer is a polymer dispersed liquid-crystal layer (8) and in that a fourth layer (4) which is made of an electrically insulating material is situated between one (7) of the second and third layers mentioned and the first layer (8).

25 9. Device according to Claim 6, characterized in that the first layer is a layer (11) which emits light under the influence of an electrical current.

10. Device according to one of the claims 5 through 9, characterized in that there is applied to one (7) of the second and third layers mentioned on the side remote from  
30 the first layer a fifth layer (2) which is provided with a predetermined pattern of electrical conductors parallel to the plane, which conductors are connected at predetermined points to preselected conductors of one (7) of the second and third layers mentioned.

11. Device according to one of the claims 5 through 10, characterized in that the pattern of electrical conductors of the other layer (7') of the second and third layers mentioned is also such that its electrical conductivity is exclusively in a direction perpendicular to the plane.
- 5
12. Device according to Claim 11, characterized in that a sixth layer (4') which is made of an electrically insulating material is situated between the other layer (7') of the second and third layers mentioned and the first layer.
- 10
13. Device according to one of Claims 11 or 12, characterized in that there is applied to the other layer (7') of the second and third layers mentioned on the side remote from the first layer a seventh layer (2') which is provided with a predetermined pattern of electrical conductors parallel to the plane, which conductors are connected at predetermined points to preselected conductors of the other layer (7') of the second and third layers mentioned.
- 15
14. Device according to one of Claims 6 to 9 inclusive, characterized in that there is applied to one (7) of the second and third layers mentioned an eighth layer (9) which is made of a photoconducting material, and there is applied to the eighth layer (9) a ninth layer (10) which is made of a transparent, electrically conducting material.
- 20
15. Device according to Claim 14, characterized in that the other layer (7') of said second and third layers is provided with electrical conductors extending exclusively in a direction perpendicular to the plane.
- 25
16. Device according to Claim 15, characterized in that a sixth layer (4') which is made of an electrically insulating material is situated between the other layer (7') of the second and third layers mentioned and the first layer.
- 30
17. Device according to one of Claims 14 to 16 inclusive, characterized in that it is provided, moreover, with a light source, for example a laser (12), for generating a light beam for exposing predetermined locations of the eighth layer (9).

18. Use of a device according to Claim 17 for manufacturing a mask for photolithographic purposes.
19. Use of the mask according to Claim 18 in a photolithographic process.
20. Device according to one of Claims 6 to 8 inclusive or 10 to 13 inclusive, characterized in that a light-sensitive layer (52) is applied on the side of the device where the other layer (7'; 10') of the second and third layers is located.
21. Printed circuit board comprising a substrate according to any of the claims 1 through 5, the substrate (7) comprising a plurality of electrodes (42) on the first surface of the substrate (7) and a plurality of electrical components (41) on the second surface of the substrate (7), the electrical components (41) having terminals electrically connected to the substrate by means of suitable connection means (43; 42, 43) aligned with predetermined ones of said electrodes (42).
22. Method of manufacturing a layer (7; 7') which extends in one plane and is provided with a pattern of electrical conductors extending exclusively in a direction perpendicular to the plane, comprising the following steps:
- selecting layer (7) from a material which has a predetermined original conductivity but which can be changed substantially by illuminating it with radiation of a predetermined wavelength;
  - providing a mask (23) on said layer (7), said mask being provided with a plurality of portions (24) non-transparent to radiation of said predetermined wavelength;
  - illuminating said mask (23) with radiation (22) of said predetermined wavelength;
  - terminating said illumination and removing said mask.
23. Method according to claim 22 wherein said material comprises either PANi/CSA or PANi/DBSA.



24. Method of manufacturing a layer (7; 7') which extends in one plane and is provided with a pattern of electrical conductors extending exclusively in a direction perpendicular to the plane, comprising the following steps:
- 5 a. applying a matrix material (31) containing solvent and a conductor additive (34) to a substrate (32);
  - b. applying an electrical field perpendicular to the substrate and evaporating the solvent at least virtually simultaneously so that the matrix material (31) acquires a solid structure and the semiconductor additive (34) forms conducting molecular structures in a direction at least virtually parallel to the electrical  
10 field;
  - c. removing the substrate (32).
25. Method of manufacturing a layer (7; 7') which extends in one plane and is provided with a pattern of electrical conductors extending exclusively in a direction  
15 perpendicular to the plane, comprising the following steps:
- a. printing a matrix material (31) containing solvent and a conductor additive (34) at predetermined locations of a substrate (32);
  - b. evaporating the solvent so that the matrix material (31) acquires an electrically conducting, solid structure;
  - 20 c. printing insulation material (36) containing a further solvent on the substrate (32) at locations situated outside the predetermined locations;
  - d. evaporating the further solvent so that the insulation material (36) acquires an electrically conducting, solid structure;
  - e. removing the substrate (32).
- 25
26. Device for displaying information, comprising at least:
- a first layer (6; 8) which extends in one plane and is composed of a material whose optical properties alter under the influence of an external electrical control system so that the transparency of portions of the first layer selected by  
30 the electrical control system alters for light incident thereon;
  - a second layer (4) which is situated on the first layer and which is made of electrically insulating material;
- characterized in that a third layer (9) which is made of photoconducting material is

applied to the second layer (4) and a fourth layer (10) which is made of transparent electrically conducting material is applied to the third layer (9).

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fig-1

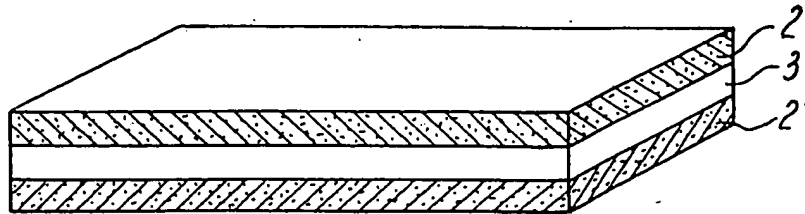


fig-2

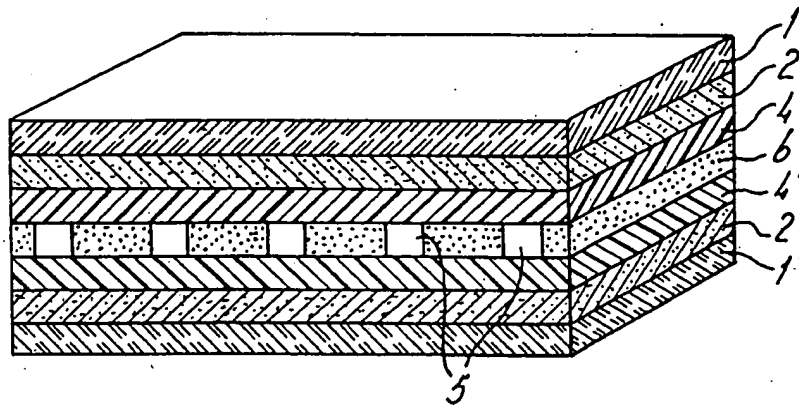
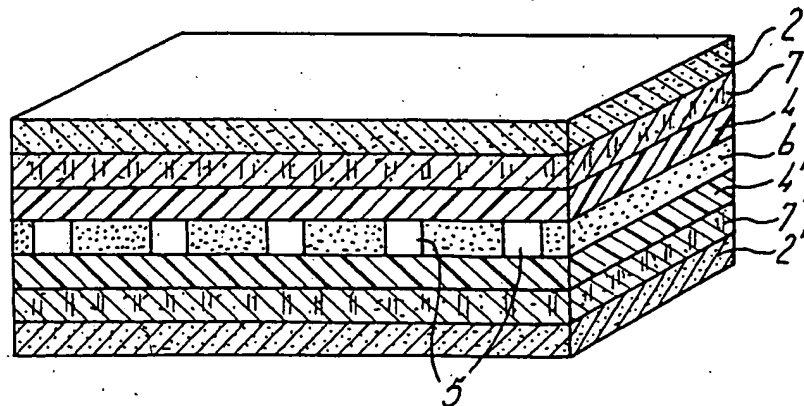


fig-3



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fig-4

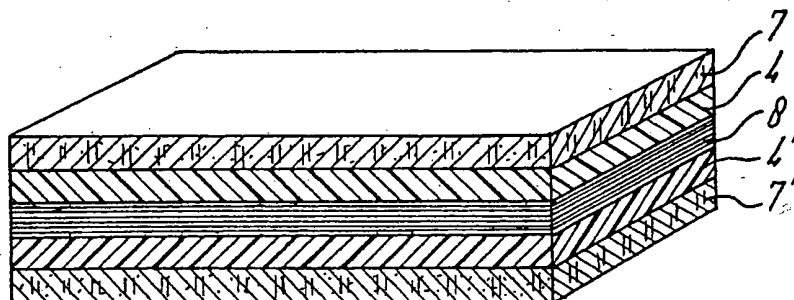


fig-5

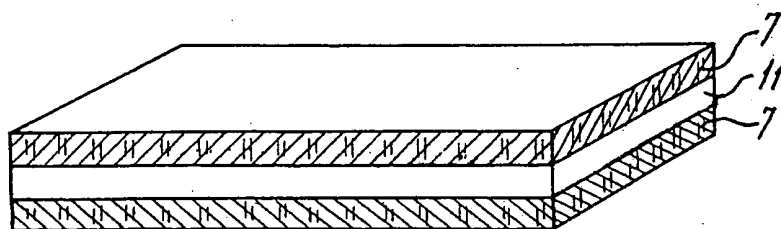


fig-6a

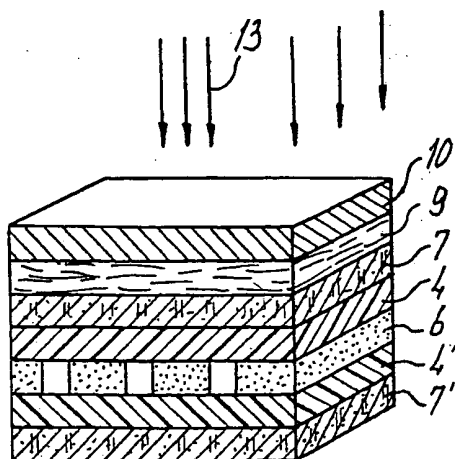
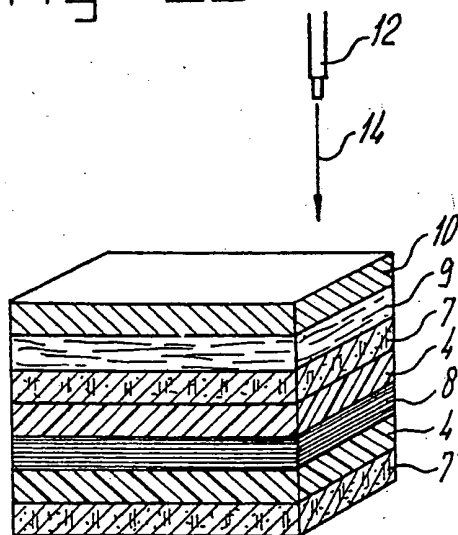
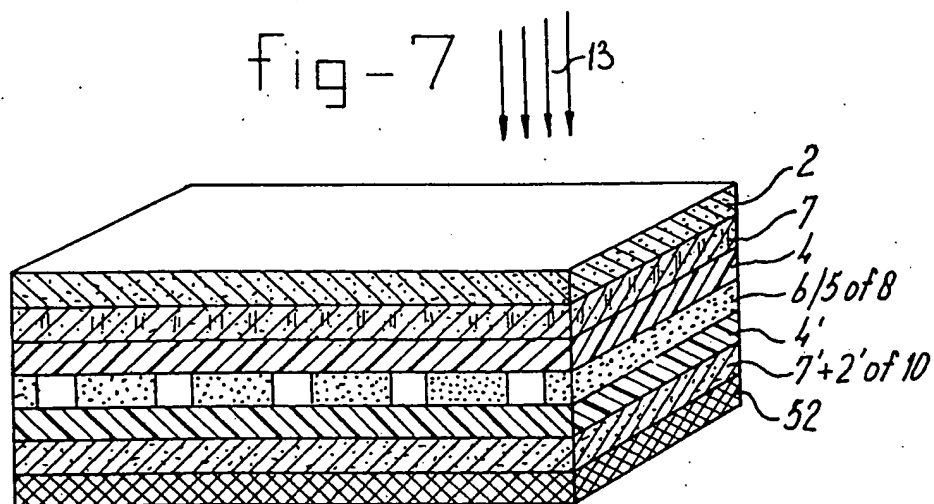


fig-6b



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fig - a

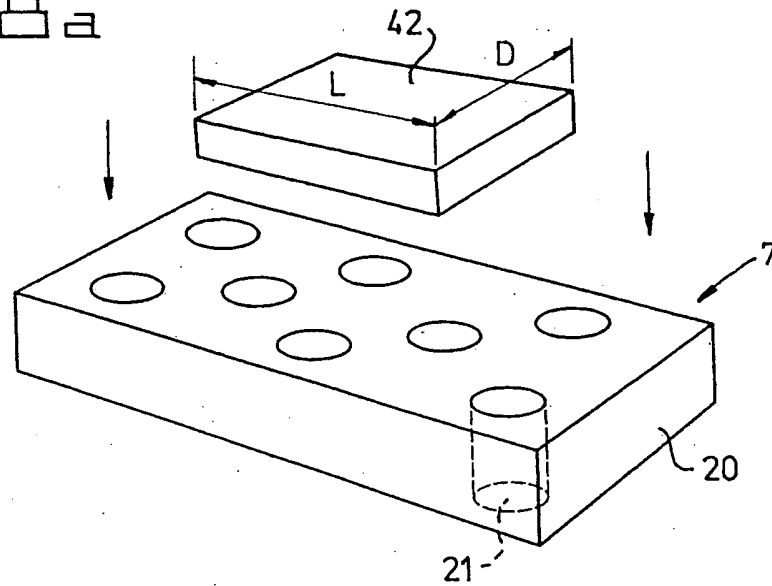


fig - b

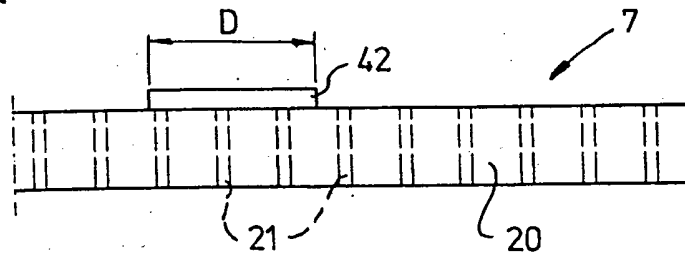


fig - c

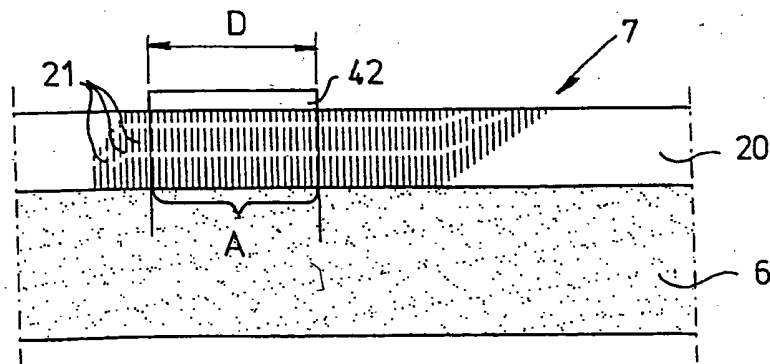


fig-9a

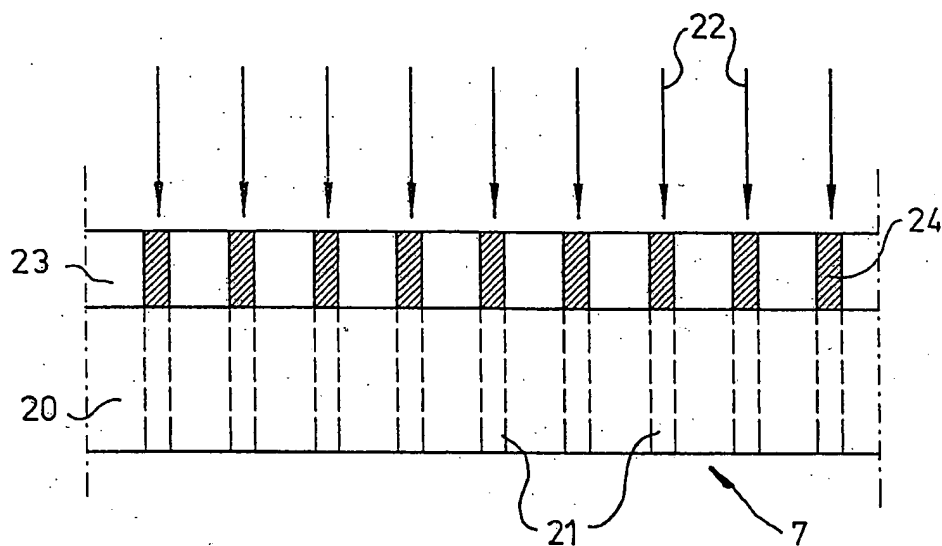
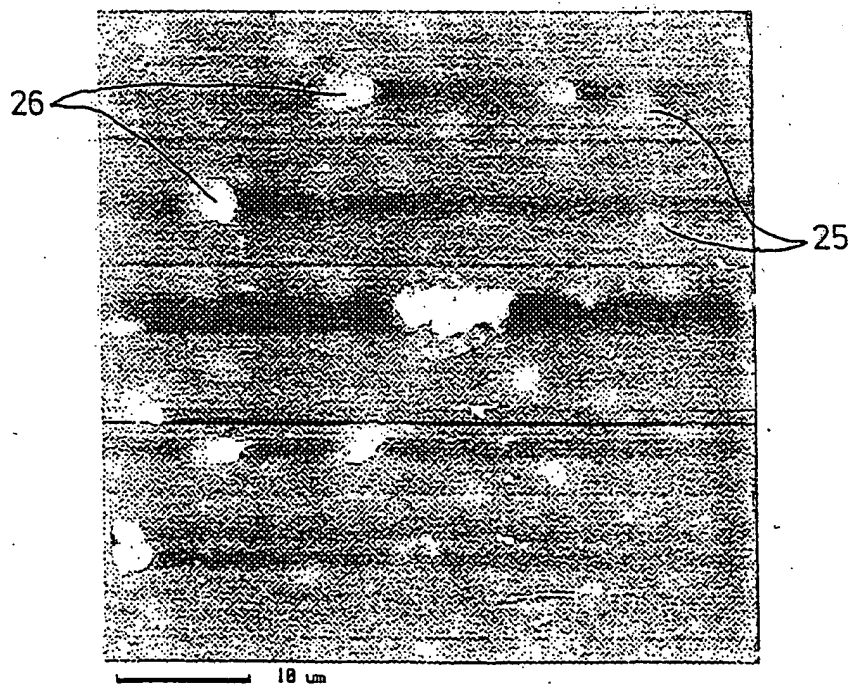


fig-9b



6/7

fig-9c

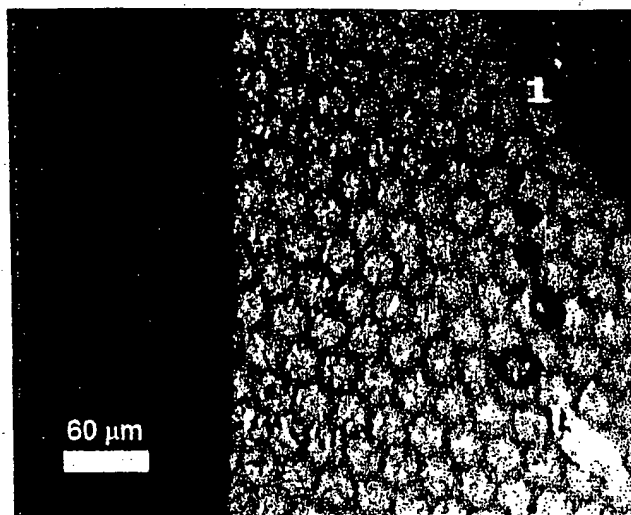
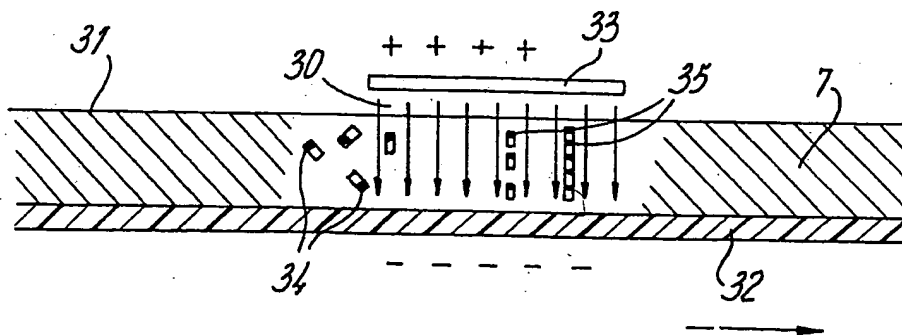


fig-10





717

fig - 11

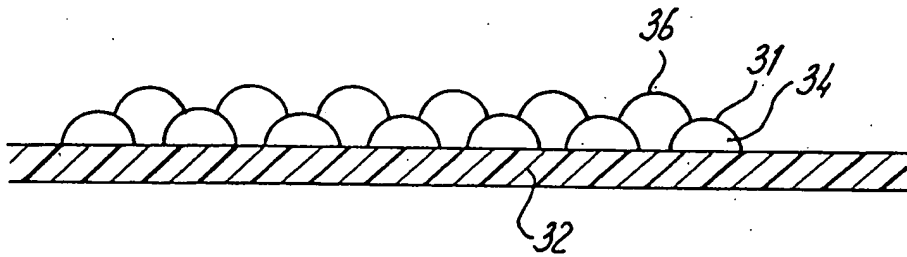
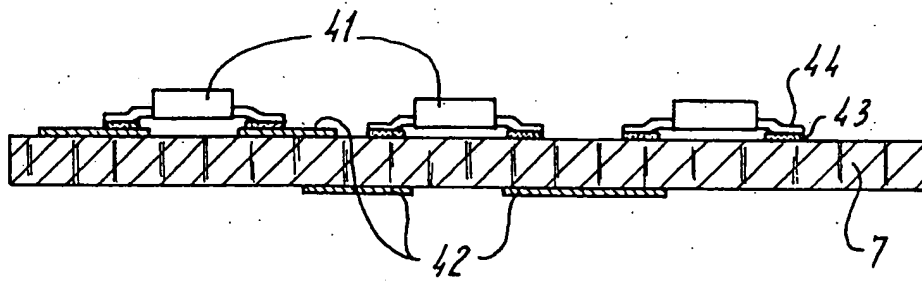


fig - 12



# INTERNATIONAL SEARCH REPORT

International Application No.  
PCT/NL 98/00341

A. CLASSIFICATION OF SUBJECT MATTER  
IPC 6 G02F1/136 H05K3/40 H01B1/20

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
IPC 6 G02F H05K H01B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 96 04585 A (GE XIAOQIN ET AL.) 15 February 1996 cited in the application	1,2,6,10
A	see page 5, last paragraph - page 6, paragraph 3; figures 2,5	21
X	EP 0 478 368 A (TEXAS INSTRUMENTS INC) 1 April 1992 see column 8, last paragraph - column 10, paragraph 1; figures 3,4,8-14	22
A	PIGGIN B P ET AL: "DIAGONAL FIBRE SUSTRATE" IBM TECHNICAL DISCLOSURE BULLETIN, vol. 21, no. 5, October 1978, page 2045 XP002055236 see the whole document	1,4,22

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

### \* Special categories of cited documents:

- \*A\* document defining the general state of the art which is not considered to be of particular relevance
- \*E\* earlier document but published on or after the international filing date
- \*L\* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- \*O\* document referring to an oral disclosure, use, exhibition or other means
- \*P\* document published prior to the international filing date but later than the priority date claimed

\*T\* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

\*X\* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

\*Y\* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

\*Z\* document member of the same patent family

Date of the actual completion of the international search

24 September 1998

Date of mailing of the international search report

17. 12. 98

Name and mailing address of the ISA

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Authorized officer

WONGEL, H

# INTERNATIONAL SEARCH REPORT

Inte. lional application No.

PCT/NL 98/00341

## Box I Observations where certain claims were found unsearchable (Continuation of item 1 of first sheet)

This International Search Report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:  
because they relate to subject matter not required to be searched by this Authority, namely:
2. ☐ Claims Nos.:  
because they relate to parts of the International Application that do not comply with the prescribed requirements to such an extent that no meaningful International Search can be carried out, specifically:
3. ☐ Claims Nos.:  
because they are dependant claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

## Box II Observations where unity of invention is lacking (Continuation of item 2 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

- |                  |                |
|------------------|----------------|
| 1. CLAIMS : 1-23 | 2. CLAIMS : 24 |
| 3. CLAIMS : 25   | 4. CLAIMS : 26 |

FOR FURTHER INFORMATION PLEASE SEE FORM PCT/ ISA/206 MAILED 07.10.98

1. ☐ As all required additional search fees were timely paid by the applicant, this International Search Report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this International Search Report covers only those claims for which fees were paid, specifically claims Nos.:
4. ☒ No required additional search fees were timely paid by the applicant. Consequently, this International Search Report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

1-23

Remark on Protest

- ☐ The additional search fees were accompanied by the applicant's protest.
- ☐ No protest accompanied the payment of additional search fees.

## INTERNATIONAL SEARCH REPORT

International Application No. PCT/NL 98/00341

FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210

### 1. Claims: 1-23

A substrate provided with a plurality of electrically conducting channels which are extending exclusively in a direction perpendicular to the surfaces of said substrate, a display device and a printed circuit board using this substrate and a method for manufacturing it by illumination of a material that becomes electrically conductive by illumination.

### 2. Claim : 24

Applying a matrix material containing a solvent and a conductor additive to a substrate, applying an electrical field perpendicular to the substrate and evaporating the solvent so that the additive forms conducting molecular structures in a direction parallel to the electrical field

### 3. Claim : 25

printing a matrix material containing a conductor additive at predetermined locations of a substrate and printing insulation material at locations outside the predetermined locations

### 4. Claim : 26

A device for displaying information comprising a layer of photoconducting material

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/NL 98/00341

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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